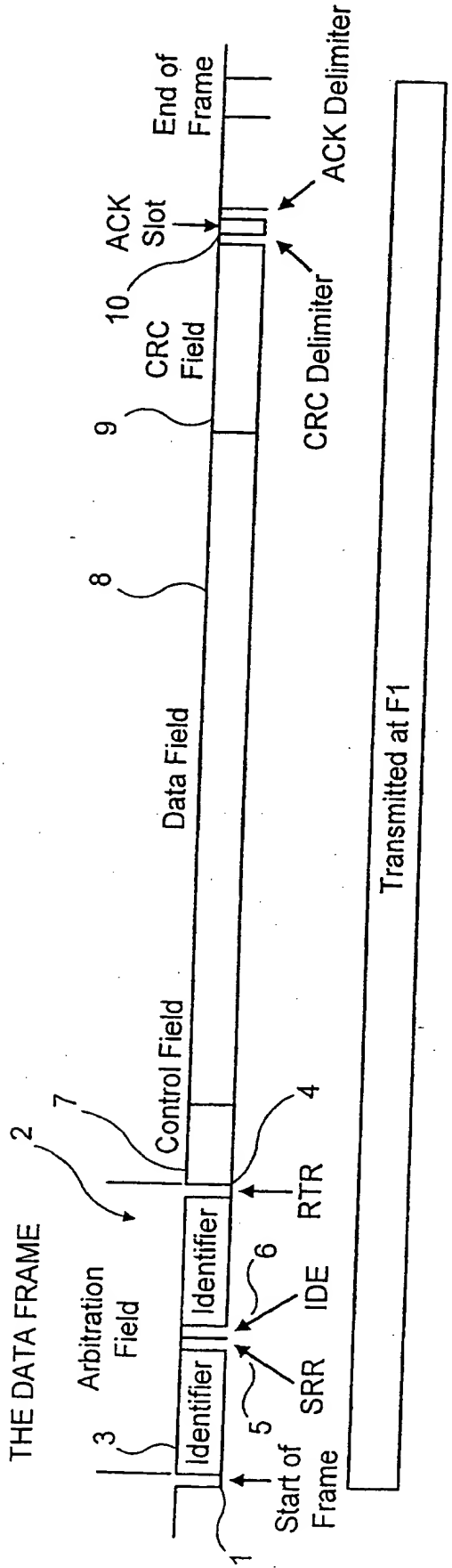

FIG. 1 PRIOR ART

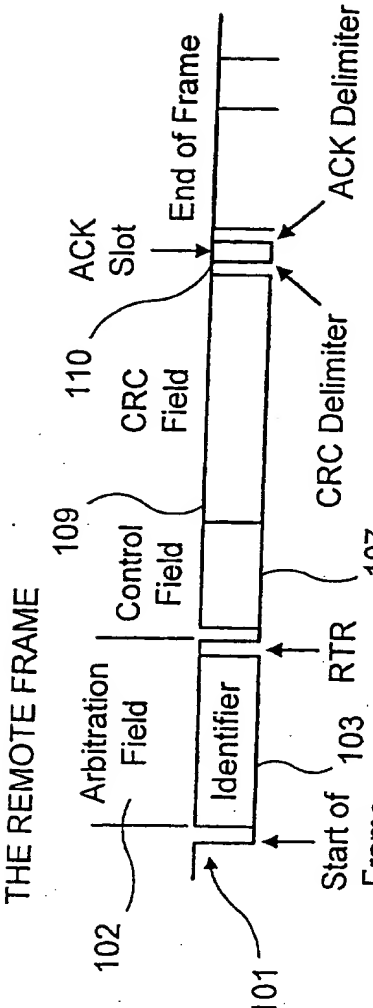

FIG. 2 PRIOR ART

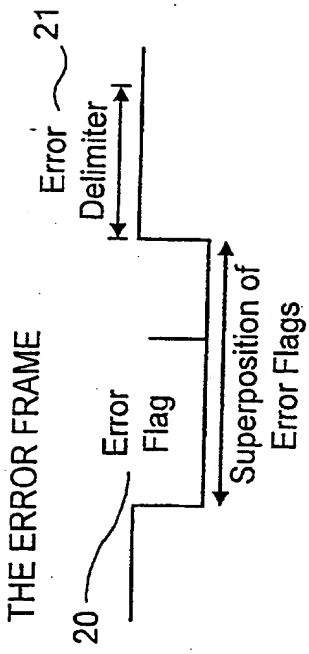

FIG. 3 PRIOR ART

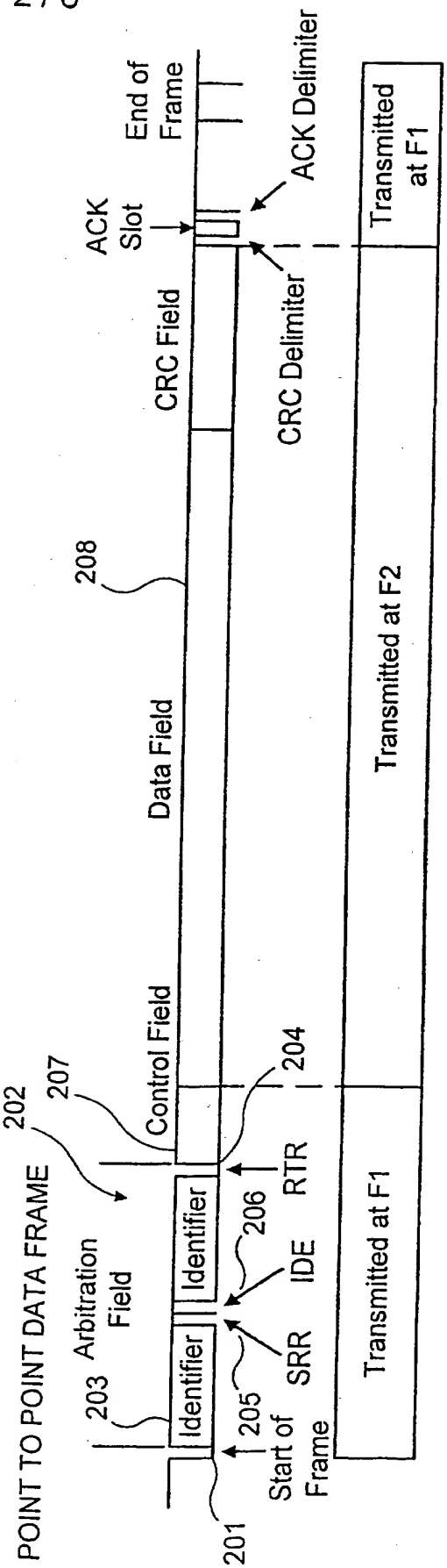

FIG. 4 PRIOR ART

METHOD OF SWITCHING FROM F1 TO F2 ETC
FLOW CHART

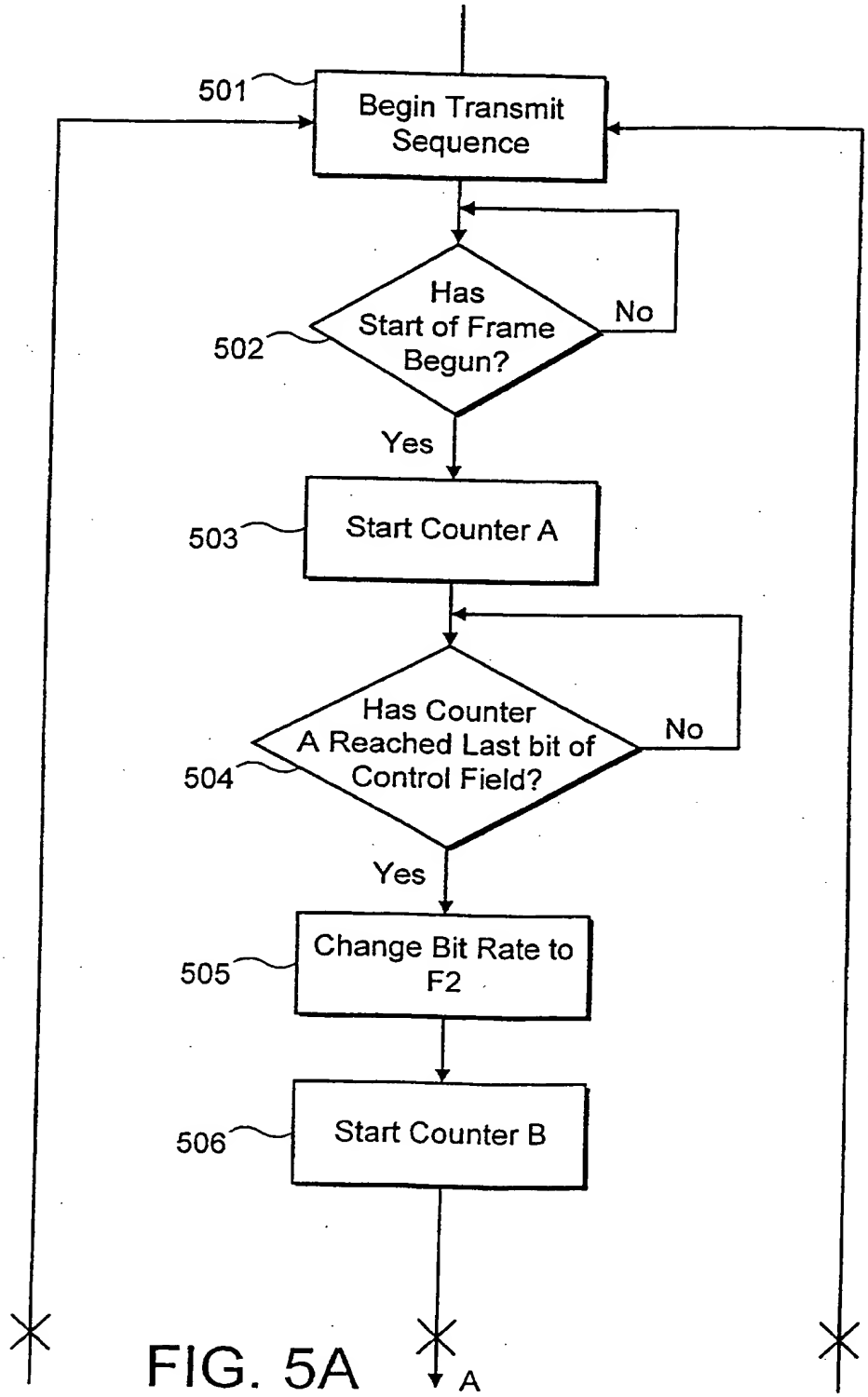

FIG. 5A

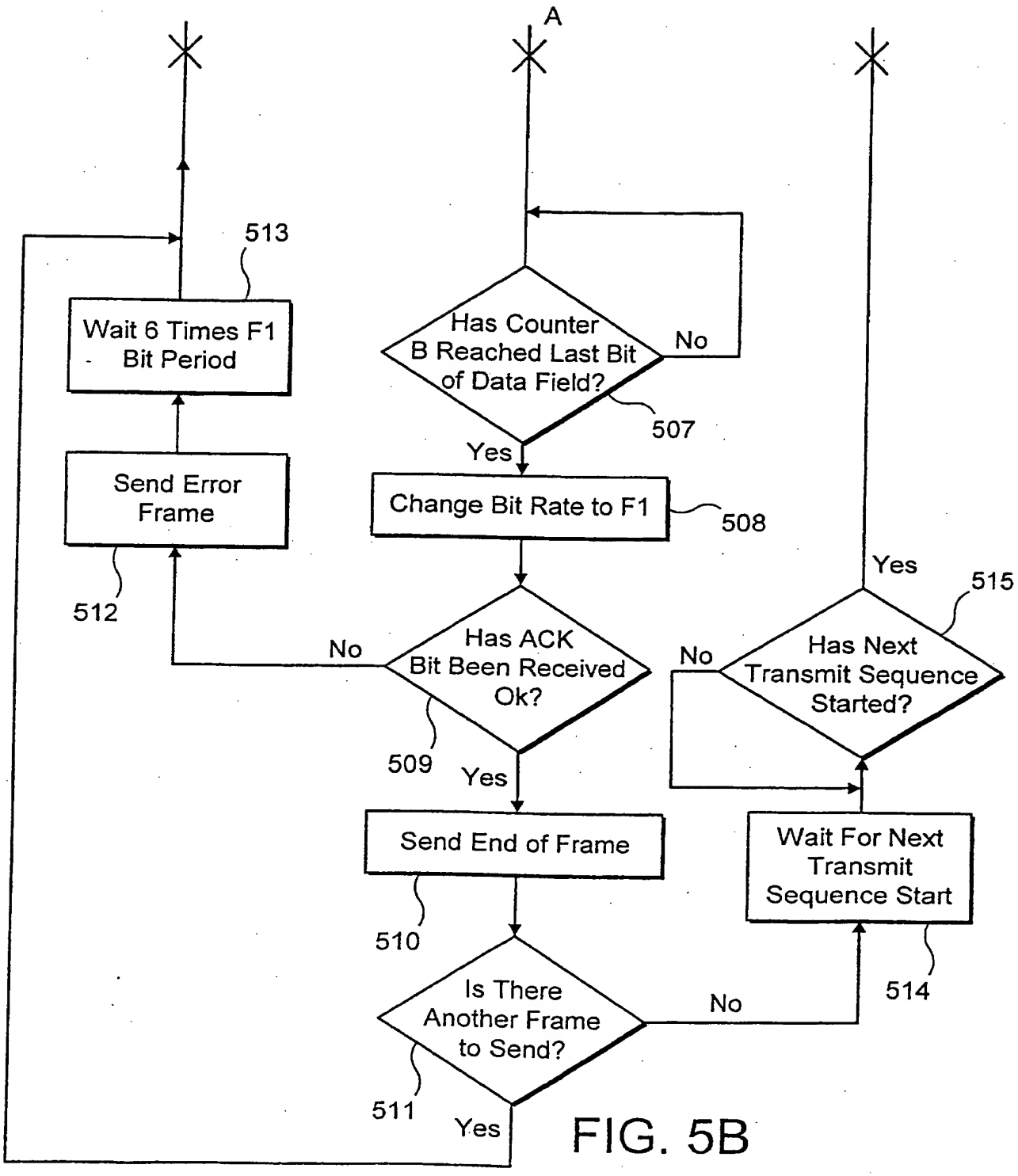
A
513
Wait 6 Times F1 Bit Period
Has Counter B Reached Last Bit of Data Field?
No
507
Yes
Send Error Frame
Change Bit Rate to F1
508
512
Yes
515
No
Has ACK Bit Been Received Ok?
No
Has Next Transmit Sequence Started?
509
Yes
Send End of Frame
Wait For Next Transmit Sequence Start
510
514
Is There Another Frame to Send?
No
511
Yes

FIG. 5B

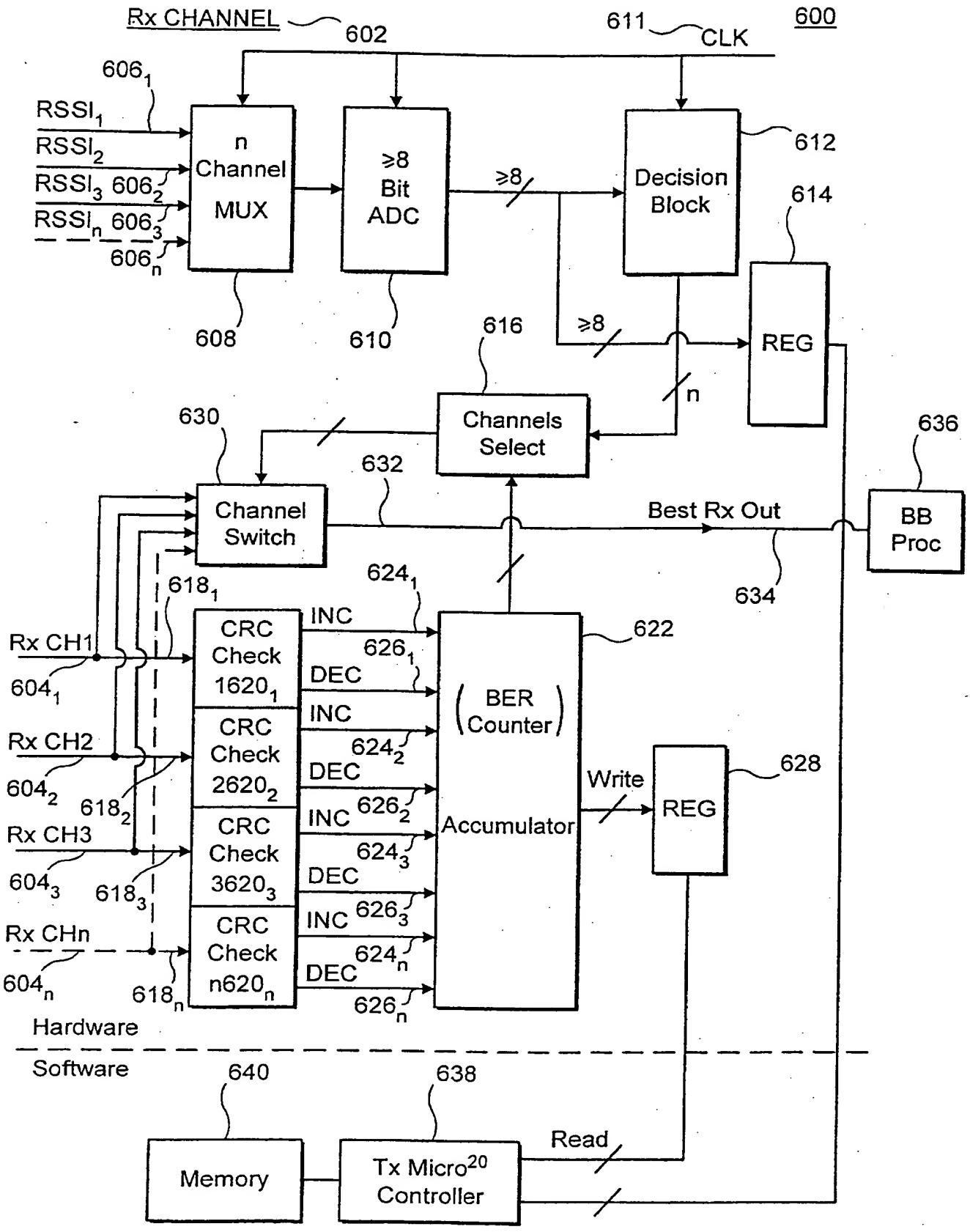

FIG. 6

AMENDED SHEET

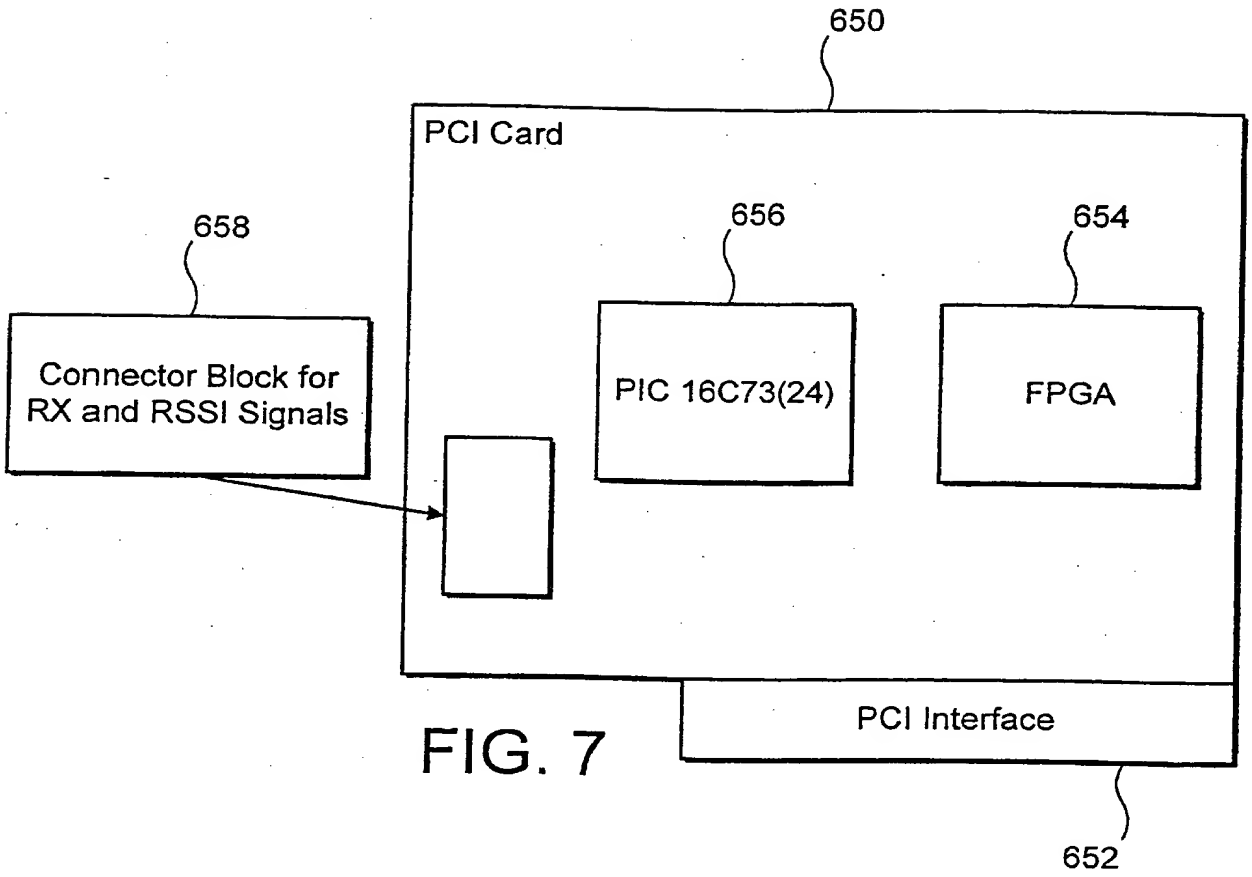
650
PCI Card
658
656
654
Connector Block for RX and RSSI Signals
PIC 16C73(24)
FPGA
PCI Interface
652

FIG. 7